



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Patent No. 5,809,336
Issue Date: September 15, 1998
Application No.: 08/484,918
Applicant(s): Moore, (et al.)
Title: High Performance Microprocessor Having Variable Speed System Clock
Docket No.: 0081-011D3

Director
Technology Center 2180
U.S. Patent and Trademark Office
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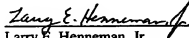
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TRANSMITTAL OF REQUEST FOR CERTIFICATE OF CORRECTION
OF PATENT FOR APPLICANTS' MISTAKE (37 CFR § 1.323)

Enclosed are the following documents for filing in the above referenced issued patent:

1. Request For Certificate of Correction of Patent For Applicants' Mistake (4 pages);
2. Form PTO/SB/44 in duplicate;
3. Power of Attorney from Technology Properties Limited (copy, 1 page);
4. Certificate Under 37 CFR § 3.73(b) from Technology Properties Limited (1 page);
5. Power of Attorney from Patriot Scientific Corporation (copy, 1 page);
6. Certificate Under 37 CFR § 3.73(b) from Patriot Scientific Corporation (1 page);
7. Power of Attorney from Charles H. Moore (1 page);
8. Certificate Under 37 CFR § 3.73(b) from Charles H. Moore (1 page);
9. Credit Card Payment Form PTO-2038 for \$100.00; and
10. Return Receipt Postcard.

Respectfully submitted,



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Dated: 6/27/06
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CERTIFICATE OF MAILING (37 CFR 1.8(A))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Director, Technology Center 2180, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: 6/27/06


Larry E. Henneman, Jr.